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Date: September 8, 2003

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TO: Commissioner for Patents FROM: Ann M. McCrackin

Attn: H. Jey Tsai Patent Examining Corps

OUR REF: 884.534US1

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Alexandria, VA 22313-1450

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FAX NUMBER 703-308-7722

* Please deliver to Examiner H. Jey Tsai in Art Unit 2812. *

Document(s) Transmitted: Response to Restriction Requirement and Preliminary Amendment (13 pages).

Total pages of this transmission, including cover letter: 14 pgs.

If you do NOT receive all of the pages described above, please telephone us at 612-373-6900, or fax us at 612-339-3061.

In re. Patent Application of: P.R. Patel et al.

Examiner: H. Tsai

Serial No.: 10/006292

Group Art Unit: 2812

Filed: December 3, 2001

Docket No.: 884.534US1

Title: ELECTRONIC ASSEMBLY WITH SANDWICHED CAPACITORS (as amended)

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Name: Ann M. McCrae

Reg. No.: Reg. No. 42,858

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

<u>S/N 10/006,292</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

P.R. Patel et al.

Examiner: H. Jey Tsai

Serial No.:

10/006,292

Group Art Unit: 2812

Filed:

December 3, 2001

Docket: 884.534US1

Title:

ELECTRONIC ASSEMBLY WITH SANDWICHED CAPACITORS (2S

amended)

Assignee:

Intel Corporation

RESPONSE TO RESTRICTION REQUIREMENT AND PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed August 15, 2003, Applicants elect, without traverse, Group I, which includes claims 1-37. Applicants respectfully cancel remaining claims 38-64 (Group II) without prejudice and reserve the right to reintroduce them in a divisional application at a later date.

Please amend this application as follows:

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TECHNOLOGY CENTER

RESPONSE TO RESTRICTION REQUIREMENT & PRELIMINARY AMENDMENT

Page 2 Dkt: 884.534USI (INTEL)

Scrial Number: 10/006,292

Filing Date: December 3, 2001

Title: ELECTRONIC ASSEMBLY WITH SANDWICHED CAPACITORS (as amended)

Assignee: Intel Corporation

IN THE TITLE

Please amend the title as follows:

ELECTRONIC ASSEMBLY WITH SANDWICHED CAPACITORS AND METHODS OF MANUFACTURE